

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

1. (currently amended) A debug device comprising:

a target board having a microcomputer equipped with an on-chip debugging function;

a debugger connected to the microcomputer via a debug interface terminal provided on the target board and that performs debugging using the on-chip debugging function implemented in the microcomputer; and

a break board that is separate from the target board, that monitors signals on an address bus, a data bus and a control bus which are output from the target board, and outputs a break signal when a predetermined break condition is satisfied.

2. (original) A debug device according to claim 1, wherein the break board is equipped with a storage section to store break conditions, and has a structure in which break conditions are written in the storage section via the target board from the debugger.

3. (currently amended) A debugging device comprising:
a target board;
a microcomputer disposed on said target board, said microcomputer being adapted to perform an on-chip debugging function;
a debug interface terminal disposed on said target board;
a debugger connected to said microcomputer via said debug interface terminal, said debugger being adapted to perform debugging using the on-chip debugging function of said microcomputer; and
a break board connected to said target board, said break board being separate from said target board and adapted to monitor signals output from said target board and to output a break signal when a predetermined break condition is satisfied.

4. (original) The debugging device of Claim 3 wherein said break board includes a storage section storing said predetermined break condition.

5. (original) The debugging device of Claim 4 wherein said predetermined break condition is written to said storage section via said target board from said debugger.